

1. General Description IS-LW08-5 product Accord with FCC CE and is 300 wireless MINI PCI EXPRESS adapter which

has lower power consumption, high linearity output power, accords with IEEE802.11B/G/N, and supports IEEE802.11i safety protocol, along with IEEE 802.11e standard service quality. It connects with other wireless device which accorded with these standards together, supports the new data encryption on 64/128 bit WEP and safety mechanism on WPA-PSK/WPA2-PSK, WPA/WPA2. Its wireless transmitting rate rises 150M, equivalent to 10 times of common 11b product. It's easy and convenient to link to wireless network for the users using desktop, laptop and other

device that needs connect to wireless network. 2. The range of applying MID, Networking camera, STB GPS, E-book, Hard disk player, Network Radios, PSP, etc, the device

3. Features

which need be supported by wireless networking.

VCC_3.3V +-0.2V

Work temperature: -20°C---70°C

Storage temperature -55°C ~ +125°C

40MHz

Feature Implementation

Clock source

Temperature range

Power supply

Package	PCI-E 52 pins
WLAN features	
General features	■CMOS MAC, Baseband PHY, and RF in a single chip for IEEE
	802.11b/g/n compatible WLAN
	■ Complete 802.11n solution for 2.4GHz band
	■72.2Mbps receive PHY rate and 72.2Mbps transmit PHY rate using
	20MHz bandwidth
	■ 300Mbps receive PHY rate and 300Mbps transmit PHY rate using
	40MHz bandwidth
	■ Compatible with 802.11n specification
	■ Backward compatible with 802.11b/g devices while operating in
	802.11n mode
Host Interface	■MINI PCI EXPRESS
Standards Supported	■ IEEE 802.11b/g/n compatible WLAN
	■ IEEE 802.11e QoS Enhancement (WMM)
	■ IEEE 802.11h TPC, Spectrum Measurement
	■802.11i (WPA, WPA2). Open, shared key, and pair-wise key authentication services
WLAN MAC Features	■ Frame aggregation for increased MAC efficiency (A-MSDU, A-MPDU)
	■ Low latency immediate High-Throughput Block Acknowledgement (HT-BA)
	■ Long NAV for media reservation with CF-End for NAV release
	■ PHY-level spoofing to enhance legacy compatibility ■ Power saving mechanism
	■ Channel management and co-existence ■ Multiple BSSID feature allows the RTL8192CE-VA4 to assume multiple MAC identities when used as a wireless bridge ■ Transmit Opportunity (TXOP) Short Inter-Frame Space (SIFS) bursting for higher multimedia bandwidth
	■ IEEE 802.11n OFDM
	■ One Transmit and one Receive path (1T1R)
	■ 20MHz and 40MHz bandwidth transmission
	■ Short Guard Interval (400ns)
	■ DSSS with DBPSK and DQPSK, CCK modulation with long and short
	preamble

■ OFDM with BPSK, QPSK, 16QAM, and 64QAM modulation.

■ Maximum data rate 54Mbps in 802.11g and 150Mbps in 802.11n

quantization noise against increased probability of clipping Fast

Maximum

3.60

1.32

1.575

800

Units

٧

mΑ

Convolutional Coding Rate: 1/2, 2/3, 3/4, and 5/6

■ Programmable scaling in transmitter and receiver to trade

Typical

3.3

1.20

1.5

■ Switch diversity for DSSS/CCK ■ Hardware antenna diversity ■ Selectable receiver FIR filters

On-chip ADC and DAC

Minimum

3.00

1.10

1.425

Description

180MA

30.0mm* 26.7mm *3 .2mm

REALTEK RTL8192CE-VA4

■ receiver Automatic Gain Control (AGC)

Interface type MINI PCI EXPRESS The range of frequency 2.4-2.484GHZ

The amount of working

The transmit distance

MENS(L*W*H)

Terms

Mode

Frequency

Data rate

Mode

Frequency

Data rate

TX mode

Rx mode

Antenna E

NO

3

5

11

13

15

17

19

21

23

25

27

29

31

33

41

43

45

47

49

51

Signal Name

WAKE#

COEX1

COEX2

GND

REFCLK-

REFCLK+

Reserved

Reserved

GND

GND

HSON

HSOP

GND

GND

HSIN

HSIP

Reserved GND

Reserved

Reserved

Reserved

Reserved

CLKREQ#

Standby mode

DC Characteristics

DC Characteristics

The chipset model

Working Power consumption

Specification: IEEE802.11b

6.DC/RF characteristics

Contents

DSSS / CCK

OFDM

min

240

180

244

2412 - 2484MHz

6.5, 13, 19.5, 26, 39, 52, 58.5, 65Mbps

7. The block diagram of product principle

NV Memory

2.4GHz

Transceiver

Typ.

250

190

RX I/Q

2412 - 2484MHz

1, 2, 5.5, 11Mbps

The supported protocol and

WLAN PHY Features

4. DC Characteristics

Parameter

3.3V I/O Supply

Voltage

Supply Voltage

1.5V Supply

Voltage

3.3V Rating

Current

5. The main performance of product

1.2V/Core

Symbol

VD33A,

VD33D

VD12A,

VD12D

VD15A,

VD15D IDD33

Item

Channel

Data Modulation	OFDM/DBPSK/DQPSK/CCK
Working Mode	Infrastructure, Ad-Hoc
The transmitting rate	300/135/54/48/36/24/18/12/9/6 /1M (self-adapting)
Spread spectrum	DSSS
Sensitivity @PER	54/135/300M:-74dBm@10%PER, 11M: <u>-85dBm@8%PER</u> 6M: <u>-88dBm@10%PER</u> , 1M: <u>-90dBm@8%PER</u>
RF Power	300M:14DBM 135M:14dBM, 54M:14dBM, 11M:18dBM
Throughput	90Mbps(external 2dbi antenna ,damping 40dbm in Shielding box)
The connect type of	External antenna

IEEE 802.11n, IEEE 802.11g,EE 802.11b

1-11 (America, Canada) ;1-13 (China, Europe) ;1-14 (Japan)

Indoor 100M, Outdoor 300M, according the local environment

max.

max.

260

200

246

unit

unit

mΑ

mΑ

Ext Interface

Configuration

Control and Memory

PCI Express

Host Interface

305 TX mode 311 320 mΑ 180 Rx mode 190 200 mΑ Standby mode 245 246 uΑ Specification: IEEE802.11g Mode OFDM Frequency 2412 - 2484MHz Data rate 6, 9, 12, 18, 24, 36, 48, 54Mbps DC Characteristics min Typ. max. unit TX mode 250 260 270 mΑ Rx mode 180 190 200 mΑ Standby mode Specification: IEEE802.11n

Тур.

DAC Circuit Baseband SPS/LDO (PHY) Regulators 40MHz 3,3V Crystal Figure 1. 11n 2x2 MAC/BB/RF Application 8. The supported platform CPU Framework Operating System Driver WIN2000/XP/VISTA/WIN7 X86 Platform Enable

Tx/Rx 2,4GHz 2.4GHz Matching Transceive

Matching

Circuit

LINUX2.4/2.6 ARM, MIPSII Enable WINCE5.0/6.0 Enable ARM, MIPSII 9. The definition of product Pin HM92C00 DAS

Top and bottom view of IS-LW08-5

NO

2

14

16

18

20

22

24

26

28

34

42

44

46

48

50

52

Description

Wake up/dormancy control

Wireless coexist control

Wireless coexist control

signal

negative

negative

negative

negative

negative

NC

NC

Reference clock request

PCI Express differential ref

CLK: 100MHz ± 300ppm

PCI Express differential tr ansmission positive signal

PCI Express differential tr ansmission negative signal

PCI Express Difference is the

PCI Express Difference is the

10. The Structure and Size of product

received positive signal

erence when reference

Signal Name

VD33A

Reserved

Reserved

Reserved

Reserved

Reserved

Reserved

W_DISABLE#

GND

PERST#

GND

Reserved

Reserved

Reserved

Reserved

Reserved

VDD15

GND

VD33

LED_WLAN#

GND

Reserved

GND

Description

3.3V power supply

negative)

NC

negative

USB negative Signal

LED PIN (Active Low)

1.5V power supply

3.3V power supply

negative

USB positive Signal

negative

negative

Radio signal control

Reset signal (low level)

received negative signal GND USB_Dnegative 36 GND 37 negative 38 USB_D+ 40 39 Reserved NC Reserved

NC

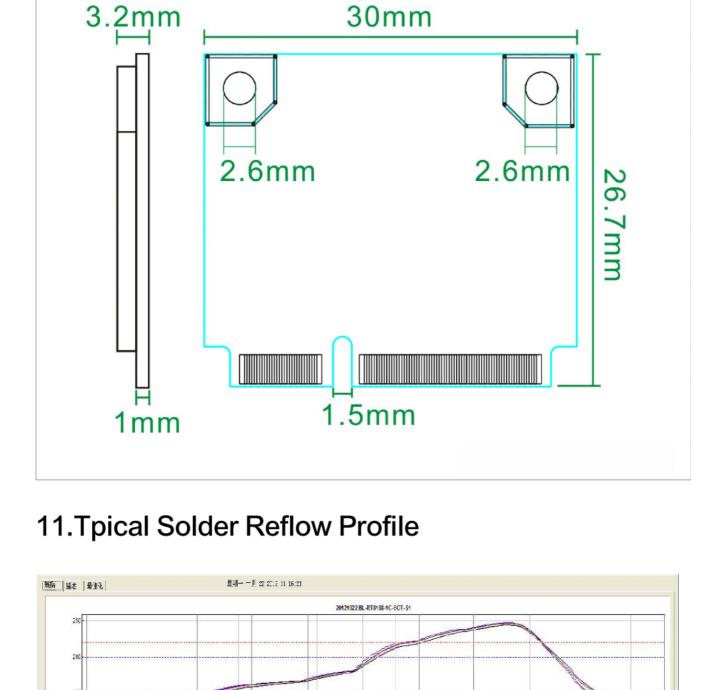
NC

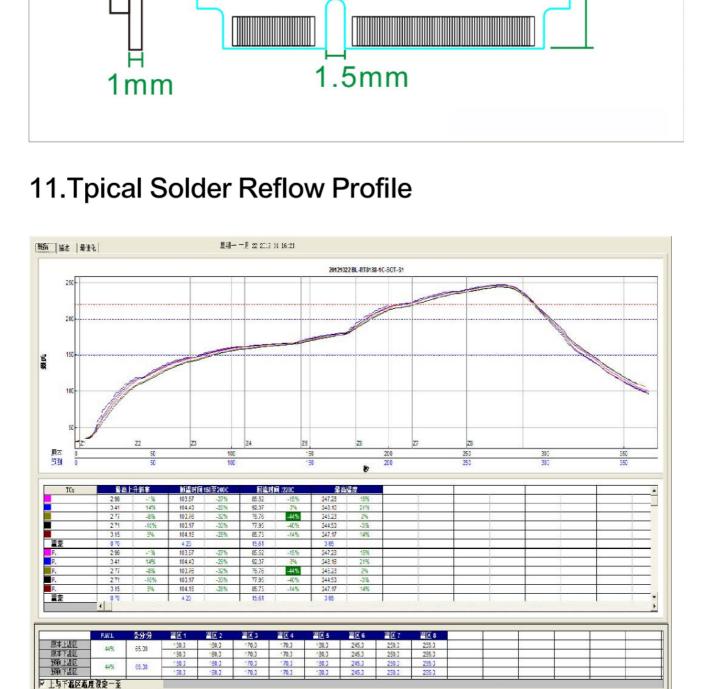
NC

NC

NC

negative





A

此程度曲线的PWI [制程界限勤数] 是=

44%

捷赫數位科技股份有限公司

Intesys Digital Technogoly Inc.

地址:新北市新店區寶橋路235巷130之2號4樓

電話:(02)8912-1668 傳真:(02)8912-1868

客服諮詢:0800@intesys.com.tw